

INFORMATION DISCLOSURE STATEMENT

Applicant	:	Naoto Tsuji, et al.
App. No.	:	Unknown
Filed	:	Herewith
For	:	INTERLAYER INSULATION FILM USED FOR MULTILAYER INTER CONNECT OF SEMICONDUCTOR INTEGRATED CIRCUIT AND METHOD OF MANUFACTURING THE SAME
Examiner	:	Unknown
Group Art Unit	:	Unknown

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Enclosed is form PTO-1449 listing two (2) references that are also enclosed.

This Information Disclosure Statement is being filed within three months of the filing date of this application or upon filing if this is a CPA or RCE, and no fee is required in accordance with 37 C.F.R. § 1.97(b)(1), (b)(2), or (b)(4).

Respectfully submitted,

KNOBBE, MARTENS, OLSON & BEAR, LLP

Dated: September 8, 2003

By: 

Katsuhiro Arai
Registration No. 43,315
Agent of Record
Customer No. 20,995
(949) 760-0404

FORM PTO-1449 U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE	ATTY. DOCKET NO. ASMJP.134AUS	APPLICATION NO. Unknown
INFORMATION DISCLOSURE STATEMENT BY APPLICANT		
(USE SEVERAL SHEETS IF NECESSARY)		
APPLICANT Naoto Tsuji, et al.		
FILING DATE Herewith		
GROUP Unknown		

EXAMINER INITIAL	OTHER DOCUMENTS (INCLUDING AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.)	

H:\DOCS\KOA\KOA-8143.DOC
090803

***EXAMINER:** INITIAL IF CITATION CONSIDERED, WHETHER OR NOT CITATION IS IN CONFORMANCE WITH MPEP 609; DRAW LINE THROUGH CITATION IF NOT IN CONFORMANCE AND NOT CONSIDERED, INCLUDE COPY OF THIS FORM WITH NEXT COMMUNICATION TO APPLICANT.